L Number	Hits	Search Text	DB	Time stamp
1	30	"5973296"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:03
2	2	"5973296" and resist$3 with temperature adj sens$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:08
3	0	("5973296" and resist$3 with temperature adj sens$3) and tw04wire adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:08
4	0	("5973296" and resist$3 with temperature adj sens$3) and two$wire adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:14
5	1	("5973296" and resist$3 with temperature adj sens$3) and controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:20
6	7	"5973296" and controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:12
7	11	"6305923"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:13
8	0	"6305923" and two$wire adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:21
9	6	"6305923" and controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:19
10	1833	219/543	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:15
12	4	(219/543 and thermal$ near spray$2) and sol$gel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:17
13	0	((219/543 and thermal$ near spray$2) and sol$gel) and controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:19

14	0	(("5973296" and resist$3 with temperature adj sens$3) and controller) and DC same Ac	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:21
15	0	219/543 and two$wire adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:21
16	0	219/543 and wire adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:21
17	3	219/543 and controller with DC and AC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:46
18	0	(219/543 and controller with DC and AC) and microprocessor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:23
19	4	("5973296" and controller) and microprocessor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:24
20	0	219/543 and controller with angle near firing	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:49
21	0	219/543 and controller with shunt adj resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:47
22	466400	219/543 and controller with shunt near2j resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:47
23	0	219/543 and controller with shunt near2 resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:47
24	466400	219/200 and controller with shunt near2j resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:48
25	0	219/200 and controller with shunt near resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:48

26	0	219/200 and controller with shunt near2 resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:48
27	131	controller with shunt near2 resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:48
28	466400	controller with shuntadj resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:49
29	106	controller with shunt adj resistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:49
30	2	(controller with shunt adj resistor) and controller with angle near firing	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:50
31	1	(controller with shunt adj resistor) and controller with firmware	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:51
32	5473	controller with firmware	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:51
33	205	(controller with firmware) and heater	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:52
34	6	((controller with firmware) and heater) and film near heater	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 11:03
11	20	219/543 and thermal$ near spray$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 10:57
35	0	(controller with shunt adj resistor) and film near heater	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 11:04
36	0	(controller with shunt adj resistor) and film near2 heater	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 11:04

37	0	(controller with shunt adj resistor) and film with heater	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 11:05
38	2	(controller with shunt adj resistor) and layer with heater	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 11:06
39	0	(controller with shunt adj resistor) and ceramic adj heater	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 11:07
40	430679	(controller with shunt adj resistor) and ceramic near2j heater	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 11:07
41	2	(controller with shunt adj resistor) and ceramic near2 heater	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/02 11:07